

	Hits	Search Text	DBs	Time Stamp
1	4334	decoupling adj capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 19:16
2	504	inter?digitated	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 19:44
3	3	S1 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/02/16 16:21
4	4	inter?digitated adj capacitance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/02/16 16:22
5	10	inter?digitated adj metal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/02/16 16:23
6	90	inter?digitated and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/04 13:15
7	38	inter?digitated and capacitor and dielectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/02/16 16:25
8	33	inter?digitated and capacitor and dielectric and circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/02/16 16:33
9	16	inter?digitated and capacitor and dielectric and circuit and plate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/02/16 16:33

	Hits	Search Text	DBs	Time Stamp
10	3892	decoupling adj capacitor and circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 19:16
11	2429	decoupling adj capacitor and circuit and integrated	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 19:16
12	1474	decoupling adj capacitor and circuit and integrated and chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 19:42
13	218	decoupling adj capacitor and "438"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 19:43
14	38	inter?digitated and "438"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 19:45
15	11	inter?digitated and capacitor and "438"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 19:45
16	34	inter?digitated same capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 20:16
17	1196	fingers same capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 20:16
18	688	fingers with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 20:16

	Hits	Search Text	DBs	Time Stamp
19	100	fingers with capacitor and metal with fingers	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 20:17
20	74	fingers with capacitor and metal with fingers and dielectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 20:17
21	1496	MIM adj (capacitor capacitors)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 20:18
22	5	fingers with capacitor and metal with fingers and dielectric and MIM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 20:18
23	47	MIM adj (capacitor capacitors) and fingers	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/03 20:19
24	6	inter?digitated and capacitor and low adj dielectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/04 13:16
25	422	438/393.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/04 14:07
26	4273	438/396.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/04 14:07
27	509	438/397.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/04 14:08

	Hits	Search Text	DBs	Time Stamp
28	911	438/239.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/04 14:08

Day : Friday  
Date: 3/4/2005  
Time: 12:13:43

# **PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = KU

First Name = JOSEPH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">07523043</a>	Not Issued	161	05/14/1990	INCREMENTAL ADVANCE SYSTEM WITH IMPROVED ACCURACY	KU, JOSEPH
<a href="#">08085967</a>	5506609	150	06/30/1993	MINIMIZING COLOR BLEED WHILE MAXIMIZING THROUGHPUT FOR COLOR PRINTING	KU, JOSEPH
<a href="#">09013775</a>	6145062	150	01/26/1998	SELECTIVE CONFLICT WRITE FLUSH	KU, JOSEPH
<a href="#">09824336</a>	6384331	150	04/02/2001	SECURED REINFORCING SUPPORT DEVICE FOR A HEAT SINK	KU, JOSEPH
<a href="#">09894142</a>	Not Issued	092	06/29/2001	MINIMIZING POWER CONSUMPTION IN PIPELINED CIRCUIT BY SHUTTING DOWN PIPELINED CIRCUIT IN RESPONSE TO PREDETERMINED PERIOD OF TIME HAVING EXPIRED	KU, JOSEPH
<a href="#">09941581</a>	6492854	150	08/30/2001	POWER EFFICIENT AND HIGH PERFORMANCE FLIP-FLOP	KU, JOSEPH
<a href="#">09988631</a>	Not Issued	094	11/20/2001	FIELD REPAIRABLE EMBEDDED MEMORY IN SYSTEM-ON-A-CHIP	KU, JOSEPH
<a href="#">10347723</a>	6826112	150	01/21/2003	LOW POWER LOGIC GATE	KU, JOSEPH
<a href="#">10348645</a>	Not Issued	041	01/21/2003	METHOD AND APPARATUS FOR SELECTING MEMORY CELLS WITHIN A MEMORY ARRAY	KU, JOSEPH
<a href="#">60631052</a>	Not Issued	018	11/24/2004	SEALED BAG OF SNACK FOOD HAVING A PORTION OF ITS PANEL REMOVABLE	KU, JOSEPH
<a href="#">10331273</a>	Not	161	12/27/2002	AIR DEODORIZER	KU, JOSEPH P.

	Issued				
<u>10800463</u>	Not Issued	018	01/01/0001	AIR DEODORIZER	KU, JOSEPH P.
<u>60342763</u>	Not Issued	159	12/28/2001	AIR DEODORIZER	KU, JOSEPH P.
<u>05928200</u>	<u>4240756</u>	150	07/26/1978	OPTIMIZED WIRE MATRIX IMPACT PRINT HEAD	KU, JOSEPH POWAH
<u>09515220</u>	<u>6351172</u>	150	02/29/2000	HIGH-SPEED OUTPUT DRIVER WITH AN IMPEDANCE ADJUSTMENT SCHEME	KU, JOSEPH W.
<u>10067317</u>	Not Issued	071	02/07/2002	METHOD AND APPARATUS FOR BUILDING UP LARGE SCALE ON CHIP DE-COUPLING CAPACITOR ON STANDARD CMOS/SOI TECHNOLOGY	KU, JOSEPH W.
<u>10836018</u>	Not Issued	030	04/29/2004	POWER MANAGEMENT USING A PRE-DETERMINED THERMAL CHARACTERISTIC OF A MEMORY MODULE	KU, JOSEPH W.
<u>60186418</u>	Not Issued	159	03/02/2000	COMPOUNDS AND METHODS	KU, JOSEPH W.
<u>08335606</u>	Not Issued	161	11/08/1994	DYNAMIC GLITCH-FREE ADDRESS DECODING	KU, JOSEPH W.
<u>09342001</u>	Not Issued	161	06/28/1999	WAFER-LEVEL PROBE PADS AND A METHOD FOR EMPLOYING THE PADS FOR EMBEDDED MEMORY TESTING	KU, JOSEPH W.
<u>09346082</u>	Not Issued	161	07/01/1999	METHOD AND APPARATUS FOR PERFORMING A DYNAMIC WAFER-LEVEL MEMORY STRESS TEST	KU, JOSEPH W.
<u>09346608</u>	<u>6347029</u>	150	07/02/1999	OVER-CURRENT PROTECTION CIRCUIT FOR LINEAR VOLTAGE REGULATORS	KU, JOSEPH W.
<u>09432534</u>	Not Issued	161	11/03/1999	SURGE CURRENT ELIMINATION CIRCUIT	KU, JOSEPH W.
<u>09865605</u>	Not Issued	095	05/29/2001	CHIP MULTIPROCESSOR WITH MULTIPLE OPERATING SYSTEMS	KU, JOSEPH WEIYEH
<u>09871504</u>	<u>6504746</u>	150	05/31/2001	HIGH-DENSITY LOW-COST READ-ONLY MEMORY CIRCUIT	KU, JOSEPH WEIYEH
<u>09892291</u>	Not Issued	094	06/25/2001	SYSTEM AND METHOD FOR CHIP TESTING	KU, JOSEPH WEIYEH

<u>09934615</u>	<u>6851064</u>	150	08/22/2001	FINE-GRAINED THERMAL CONTROL IN MEMORY SUBSYSTEMS	KU, JOSEPH WEIYEH
<u>09934891</u>	Not Issued	094	08/22/2001	DIGITAL EVENT SAMPLING CIRCUIT AND METHOD	KU, JOSEPH WEIYEH
<u>09935021</u>	<u>6600328</u>	150	08/22/2001	ANALOG METHOD AND CIRCUIT FOR MONITORING DIGITAL EVENTS PERFORMANCE	KU, JOSEPH WEIYEH
<u>10655892</u>	Not Issued	030	09/04/2003	METHOD AND APPARATUS FOR THERMALLY ASSISTED TESTING OF INTEGRATED CIRCUITS	KU, JOSEPH WEIYEH
<u>29119829</u>	Not Issued	161	03/09/2000	ROLLING GLUE AND PRIMER APPLICATOR TOOL	KUBASAK, JOSEPH L
<u>08239894</u>	Not Issued	161	05/09/1994	LONG LENGTH CONDUIT AND OTHER SUCH BUILDING MATERIALS PIVOTING ELEVATOR DOLLY CART APPARATUS	KUBASAK, JOSEPH L.
<u>29020204</u>	Not Issued	161	03/21/1994	J AND J FLASHLIGHT SAFETY CLIP	KUBASAK, JOSEPH L.
<u>29021069</u>	Not Issued	161	04/08/1994	J AND J PVC GLUE ROLLAR APPLICATOR BRUSH LID	KUBASAK, JOSEPH L.
<u>29021224</u>	Not Issued	161	04/11/1994	J AND J TAILGATE SAVER BRACKETS	KUBASAK, JOSEPH L.
<u>29024976</u>	Not Issued	161	06/24/1994	JAMER FUNNEL COUPLING	KUBASAK, JOSEPH L.
<u>07100212</u>	Not Issued	161	09/23/1987	FATTY ACID ESTERS OF GLYCEROL AND POLYGLYCEROL PLASTICIZED POLYURETHANE ELASTOMERS	KUBEC, JOSEPH B.
<u>07100290</u>	<u>4766167</u>	150	09/23/1987	HYDROXY ACID ESTER PLASTICIZED URETHANE ELASTOMERS	KUBEC, JOSEPH B.
<u>08747846</u>	<u>5971557</u>	150	11/13/1996	ELECTROLUMINESCENT BACKLIT KEYPAD	KUBES, JOSEPH
<u>08946270</u>	<u>6035180</u>	150	10/07/1997	COMMUNICATION MODULE HAVING SELECTIVELY PROGRAMMABLE EXTERIOR SURFACE	KUBES, JOSEPH
<u>08946491</u>	<u>5877695</u>	150	10/07/1997	VISUAL ALARM FOR A COMMUNICATION MODULE	KUBES, JOSEPH
<u>09348539</u>	Not	161	07/07/1999	MULTIMEDIA ADVERTISING	KUBICEK,

	Issued			MARKER	JOSEPH
<u>08952451</u>	<u>5998899</u>	150	11/20/1997	MAGNETIC BEARING SYSTEM INCLUDING A CONTROL SYSTEM FOR A FLYWHEEL AND METHOD FOR OPERATING SAME	KUBICKY, JOSEPH J
<u>08752593</u>	<u>5708312</u>	150	11/19/1996	MAGNETIC BEARING SYSTEM INCLUDING A CONTROL SYSTEM FOR A FLYWHEEL AND METHOD FOR OPERATING SAME	KUBICKY, JOSEPH J.
<u>09190561</u>	<u>6324651</u>	150	11/12/1998	METHOD AND APPARATUS FOR SAVING DEVICE STATE WHILE A COMPUTER SYSTEM IS IN SLEEP MODE	KUBIK, JOSEPH
<u>09321786</u>	<u>6611915</u>	150	05/27/1999	SELECTIVE LOADING OF CLIENT OPERATING SYSTEM IN A COMPUTER NETWORK	KUBIK, JOSEPH
<u>09321790</u>	<u>6519633</u>	150	05/27/1999	INSTALLABLE FILE SYSTEM FOR CLIENT IN A COMPUTER NETWORK	KUBIK, JOSEPH
<u>09439050</u>	<u>6538660</u>	150	11/12/1999	METHOD, SYSTEM, AND PROGRAM FOR SUPERIMPOSING DATA FROM DIFFERENT APPLICATION PROGRAMS	KUBIK, JOSEPH
<u>09448458</u>	<u>6344863</u>	150	11/24/1999	THREE-DIMENSIONAL GUI WINDOWS WITH VARIABLE-SPEED PERSPECTIVE MOVEMENT	KUBIK, JOSEPH
<u>09652366</u>	<u>6775691</u>	150	08/31/2000	SERVER PROTOCOL FOR DEAD E-MAIL IDENTIFICATION LOCATOR	KUBIK, JOSEPH

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## Inventor Name Search Result

Your Search was:

Last Name = VOORDE

First Name = PAUL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09203445</a>	<a href="#">6111300</a>	150	12/01/1998	MULTIPLE COLOR DETECTION ELEVATED PIN PHOTO DIODE ACTIVE PIXEL SENSOR	VOORDE, PAUL J. VANDE
<a href="#">09383106</a>	Not Issued	161	08/25/1999	BIASING CONNECTION FOR AN ARRAY OF PHOTODIODES	VOORDE, PAUL J. VANDE
<a href="#">07146068</a>	<a href="#">4894694</a>	150	01/20/1988	MOSFET STRUCTURE AND METHOD FOR MAKING SAME	VOORDE, PAUL V.
<a href="#">07307876</a>	Not Issued	166	02/07/1989	METHOD OF FORMING A BIPOLAR TRANSISTOR PORTION COMPRISING A SELF-ALIGNED TRENCH-ISOLATED EMITTER	VOORDE, PAUL V.
<a href="#">07510637</a>	<a href="#">5008210</a>	250	04/18/1990	PROCESS OF MAKING A BIPOLAR TRANSISTOR WITH A TRENCH-ISOLATED EMITTER	VOORDE, PAUL V.
<a href="#">07595725</a>	<a href="#">5144403</a>	150	10/09/1990	BIPOLAR TRANSISTOR WITH TRENCH-ISOLATED EMITTER	VOORDE, PAUL V.
<a href="#">10067317</a>	Not Issued	071	02/07/2002	METHOD AND APPARATUS FOR BUILDING UP LARGE SCALE ON CHIP DE-COUPLING CAPACITOR ON STANDARD CMOS/SOI TECHNOLOGY	VOORDE, PAUL VANDE
<a href="#">10228231</a>	<a href="#">6709943</a>	150	08/26/2002	SEMICONDUCTOR DIFFUSED RESISTORS WITH OPTIMIZED TEMPERATURE DEPENDENCE	VOORDE, PAUL VANDE
<a href="#">06926318</a>	<a href="#">4746624</a>	150	10/31/1986	METHOD FOR MAKING AN LDD MOSFET WITH A	VOORDE, PAUL VANDE

				SHIFTED BURIED LAYER AND A BLOCKING REGION	
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**Inventor Search Completed: No Records to Display.**

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	VOORDE	PAUL	

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